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Substitute for form 1449/PTO				Complete if Known	
				Application Number	10/622,498-Conf. #4995
				Filing Date	July 21, 2003
				First Named Inventor	Tadashi Iwasaki
				Art Unit	2819
				Examiner Name	C. L. Hammond
Sheet	1	of	1	Attorney Docket Number	
				N0029.1644	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
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					<input checked="" type="checkbox"/> T ⁶

Examiner Signature	Date Considered
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
/C.H.	CA	"A 660 MB/s Interface Megacell Portable Circuit in 0.3 μm-0.7 μm CMOS ASIC", IEEE Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996.			
/C.H.	CB	"Design Guide For A Low Speed Buffer For The Universal Serial Bus", Revision 1.1 December, 1996 Intel Corporation.			

Examiner Signature	/Crystal Hammond/	Date Considered	01/03/2008
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